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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/576,570	04/19/2006	2819	900	FR03 0127 US1	3	7	3

## CONFIRMATION NO. 3885

24738  
PHILIPS ELECTRONICS NORTH AMERICA CORPORATION  
INTELLECTUAL PROPERTY & STANDARDS  
1109 MCKAY DRIVE, M/S-41SJ  
SAN JOSE, CA95131

## FILING RECEIPT

Date Mailed: 12/18/2006

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

### Applicant(s)

Emmanuel Alie, Sophia Antipolis-Valbonne,  
FRANCE;

### Assignment For Published Patent Application

KONINKLIJKE PHILIPS ELECTRONICS N.V., EINDHOVEN, NETHERLANDS

**Power of Attorney:** The patent practitioners associated with Customer Number 24738

### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/IB04/03407 10/18/2004

### Foreign Applications

EUROPEAN PATENT OFFICE (EPO) 03300175.1 10/22/2003

### If Required, Foreign Filing License Granted: 12/15/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/576,570**

**Projected Publication Date:** 03/29/2007

**Non-Publication Request:** No

**Early Publication Request:** No

**Title**

Method and system for powering an integrated circuit

**Preliminary Class**

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